

WHAT IS CLAIMED IS:

1. An error correction apparatus comprising:
a receiving section configured to receive a
signal;

5 an amplifier section configured to amplify the
received signal received by the receiving section;

an automatic gain control section configured to
generate a control signal and to control a gain of the
amplifier section based on the generated control signal,
10 so as to maintain the amplified received signal at a
predetermined level; and

a turbo decoder configured to execute an error
correction process on the received signal to thereby
output a decoding result of the executed received
15 signal based on the generated control signal.

2. The error correction apparatus according to
claim 1, the turbo decoder including a decoding section
and a multiplication section coupled to the decoding
section, configured to multiply the received signal by
20 the generated control signal, so that the received
signal has a reverse property of the generated control
signal before the received signal is supplied to the
decoding section of the turbo decoder.

3. The error correction apparatus according to
25 claim 1, wherein the turbo decoder including a
calculating section configured to calculate a path
metric of the received signal, and an outputting

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